

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:
Iain Robertson, et al.

Docket No.: TI-28925

Art Unit: TBD

Serial No.: TBD

Examiner: TBD

Filed: Herewith

For: Communications Interface Between Clock Domains with Minimal Latency

LETTER TO THE OFFICIAL DRAFTSPERSON

Assistant Commissioner for
Patents
Washington, D. C. 20231

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EL547744725US. I hereby certify that this paper is being
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J. Dennis Moore, Reg. No. 28,885

1/5/01
Date

Dear Sir:

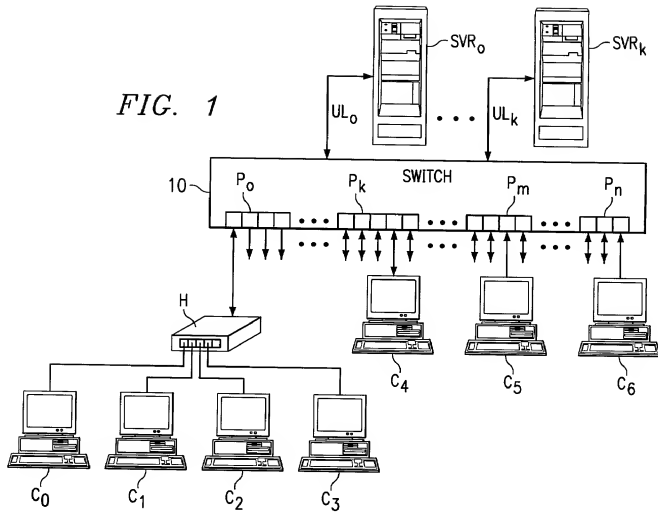
Submitted herewith is a set of formal drawings (5 sheets) for the above referenced case. Please charge any necessary fees to Deposit Account No. 20-0668 of Texas Instruments Incorporated. This sheet is enclosed in triplicate.

Respectfully submitted,

J. Dennis Moore
Attorney for Applicants
Reg. No. 28,885

Texas Instruments Incorporated
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Dallas, Texas 75265
(972) 917-5646

FIG. 1



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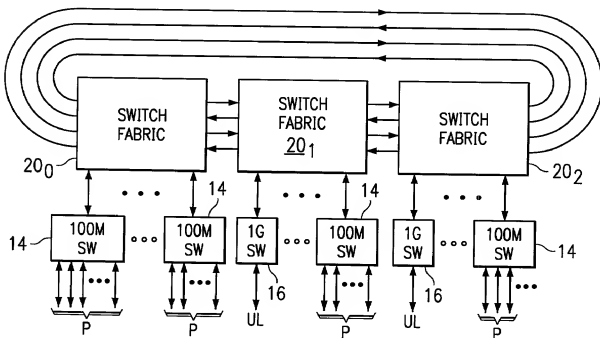
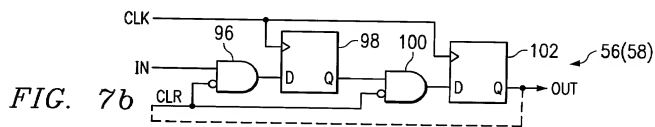
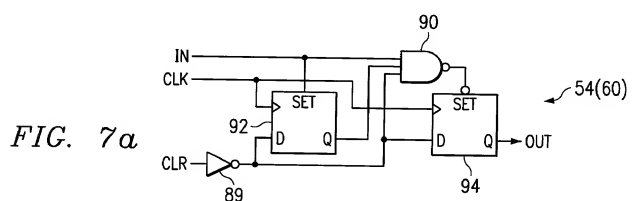
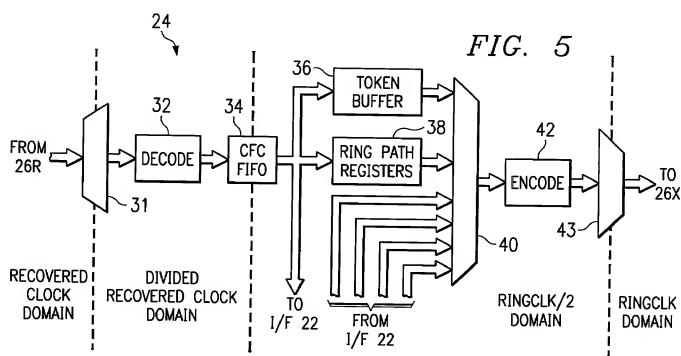
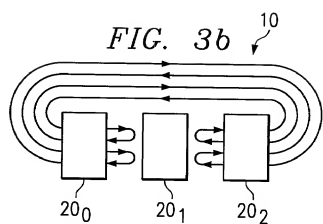
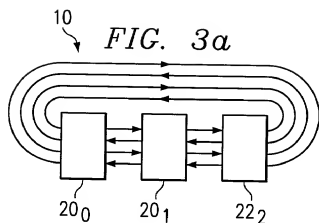


FIG. 2



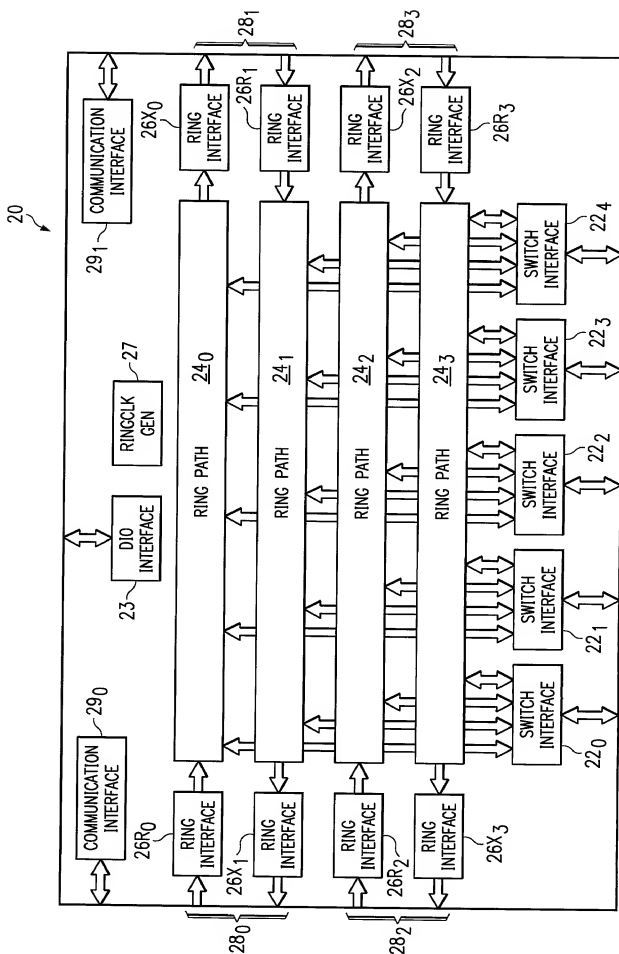
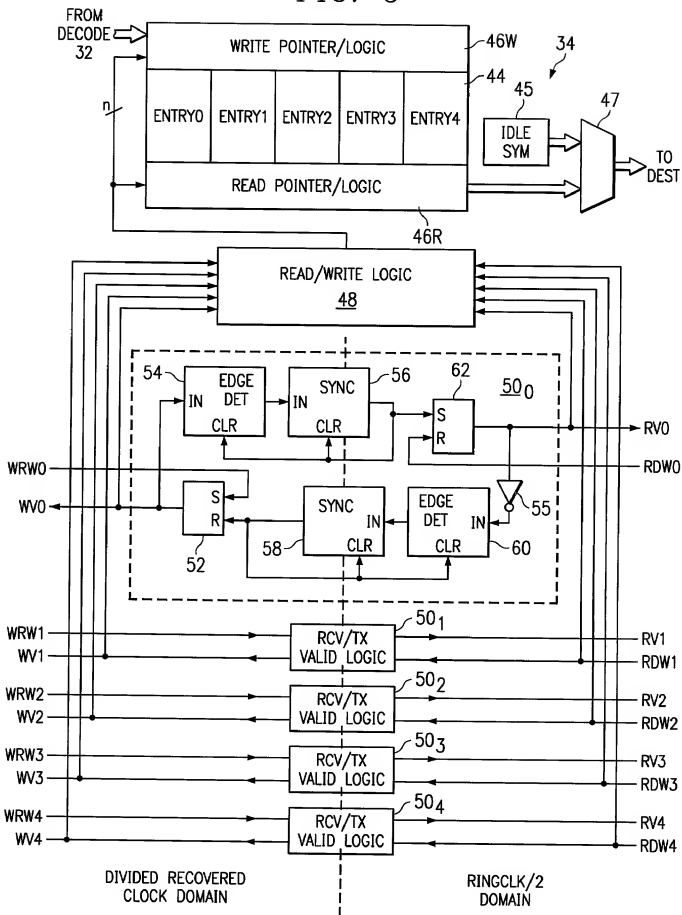


FIG. 4

FIG. 6



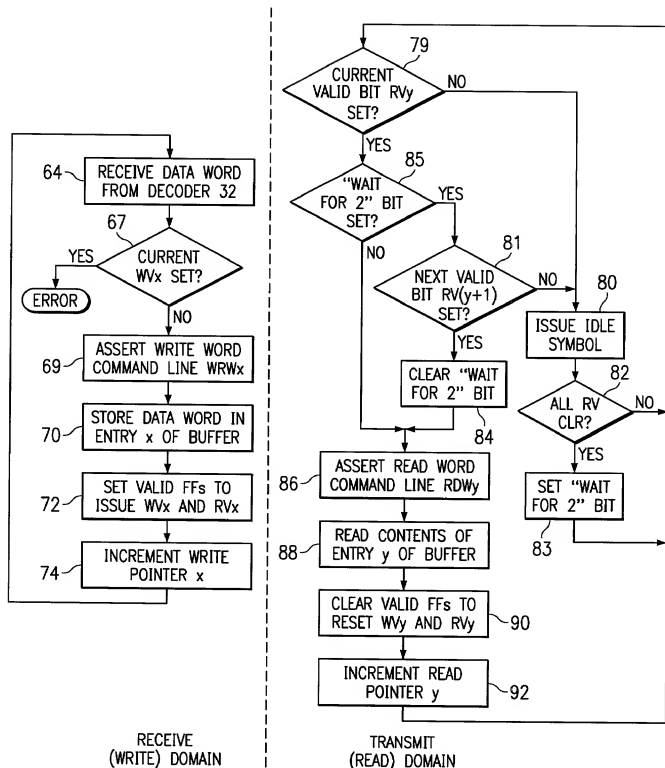


FIG. 8